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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/816,126	04/01/2004	Vincent Rabary	S1022.81105US00	9639
23628	7590	07/13/2005	EXAMINER	
WOLF GREENFIELD & SACKS, PC FEDERAL RESERVE PLAZA 600 ATLANTIC AVENUE BOSTON, MA 02210-2211			MAI, LAM T	
			ART UNIT	PAPER NUMBER
			2819	

DATE MAILED: 07/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

AK

<b>Office Action Summary</b>	<b>Application No.</b> 10/816,126	<b>Applicant(s)</b> RABARY ET AL.	
	<b>Examiner</b> LAM T. MAI	<b>Art Unit</b> 2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 01 April 2004.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2,4,5,8,11,12,14-16,18 and 19 is/are rejected.
- 7) ☒ Claim(s) 3,6,7,9,10,13,17 and 20 is/are objected to.
- 8) ☒ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>4/1/04</u> . | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Priority***

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Specification***

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

### ***Claim Objections***

Claim 4 is objected to because of the following informalities: The limitation "second amplifier" does not specify in claim parent claim. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1-2, 4-5, 8, 11-12, 14-16, and 18-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Ghaffaripour et al (USP 5648742).

Regarding claim 1, Ghaffaripour discloses a power amplifier circuit comprising at least one first amplifier (103) having a first input (111) receiving an input voltage through at least one first coupling capacitor (112) and connected to an output of the first amplifier, and having a second input (103), separate from the first input, receiving a reference voltage supplied by a time constant circuit comprising a decoupling capacitor (140), at least one first controllable switch (142) connecting the first and second inputs (see figure 2, col. 3-6).

Regarding claim 2, Ghaffaripour teaches the first controllable switch connects directly the first and second inputs (see figure 2, col. 3-6).

Regarding claim 4, Ghaffaripour teaches a second amplifier (124) having first and second inputs and an output, the first input of the second amplifier receiving the outputs of the first and second amplifiers, the second inputs of the first and second amplifiers being interconnected and the outputs of the first and second amplifiers being connected to respective terminals of a load (see figure 2, col. 3-6).

Regarding claim 5, Ghaffaripour teaches the second input is connected to the midpoint of a series connection between high and low supply terminals of first and second resistors (see figure 2, col. 3-6).

Regarding claim 8, Ghaffaripour discloses a first amplifier (103) having a first input, a second input and an output; a first coupling capacitor (112), connected to the first input, for receiving an input voltage; a reference voltage circuit (103) for supplying a reference voltage to the second input; and a controllable switch (142) connected between the first and second inputs (see figure 2, col. 3-6).

Regarding claim 11, Ghaffaripour teaches the reference voltage circuit comprises first (138) and second (136) resistors forming a resistive divider, a junction point (108) of the resistive divider connected to the second input, the reference voltage circuit further comprising a decoupling capacitor (140) connected to the junction point of the resistive divider (see figure 2, col. 3-6).

Regarding claim 12, Ghaffaripour teaches a second controllable switch (146) connected in series with the second resistor (136), wherein the first and second controllable switches have inverse control logic (see figure 2, col. 3-6).

Regarding claim 14, Ghaffaripour teaches a second amplifier (124) having first and second inputs and an output, the first input of the second amplifier receiving the outputs of the first and second amplifiers, the second inputs of the first and second amplifiers being interconnected and the outputs of the first and second amplifiers being connected to respective terminals of a load (see figure 2, col. 3-6).

Regarding claim 15, Ghaffaripour teaches the reference voltage circuit comprises first and second resistors connected in series between high and low supply voltages to form a resistive divider, a junction point of the resistive divider connected to the second inputs of the first and second amplifiers, and a decoupling capacitor connected to a junction point of the resistive divider (see figure 2, col. 3-6).

Regarding claim 16, Ghaffaripour teaches a second controllable switch (146) connected in series with the second resistor, the first and second controllable switches having inverse control logic (see figure 2, col. 3-6).

Regarding claim 18, Ghaffaripour discloses a technique for operating a power amplifier comprising a first amplifier (103) having a first input (111), a second input and an output, a first coupling capacitor (112) connected to the first input and a reference voltage circuit connected to the second input, said method comprising: receiving an input voltage through the coupling capacitor; receiving a reference voltage at the second input; and connecting the first and second inputs together in a standby mode (142)(see figure 2, col. 3-6).

Regarding claim 19, Ghaffaripour teaches the first and second inputs comprises applying a standby signal to a first controllable switch connected between the first and second inputs (see figure 2, col. 3-6).

#### ***Allowable Subject Matter***

Claims 3, 6-7, 9-10, 13, 17, and 20 are objected to as being dependent upon a rejected base claim, but they would be considered for allowable if they are rewritten in independent form including all of the limitations of the base claim and any intervening claims. The prior art fails to teach or suggest features claim in the objected claims.

#### ***Conclusion***

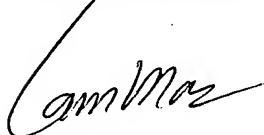
Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 6:00 am - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Pascal J. Robert can be reached on (571) 272-1769. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Lam T. Mai  
Art Unit 2819